



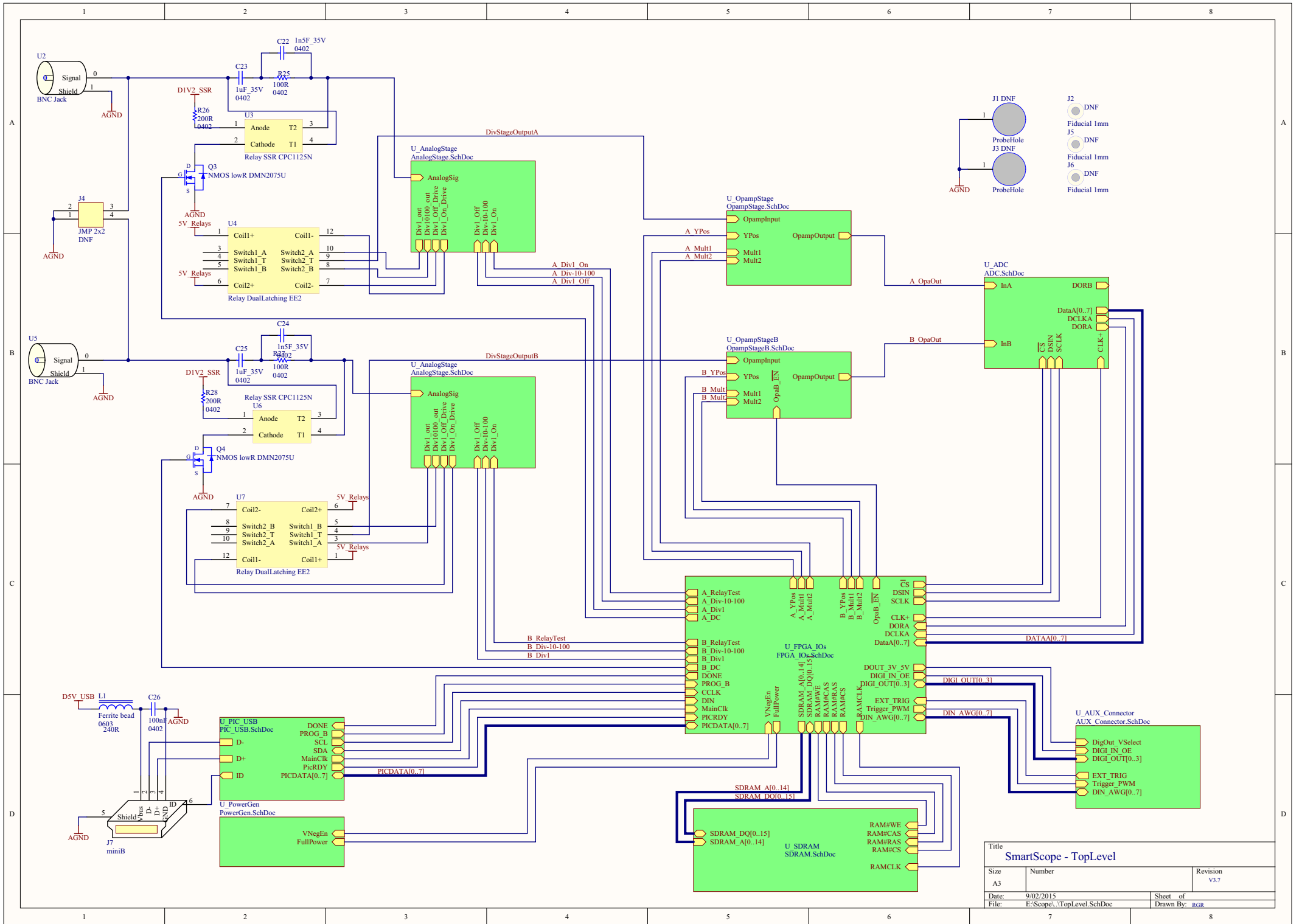
SmartScope

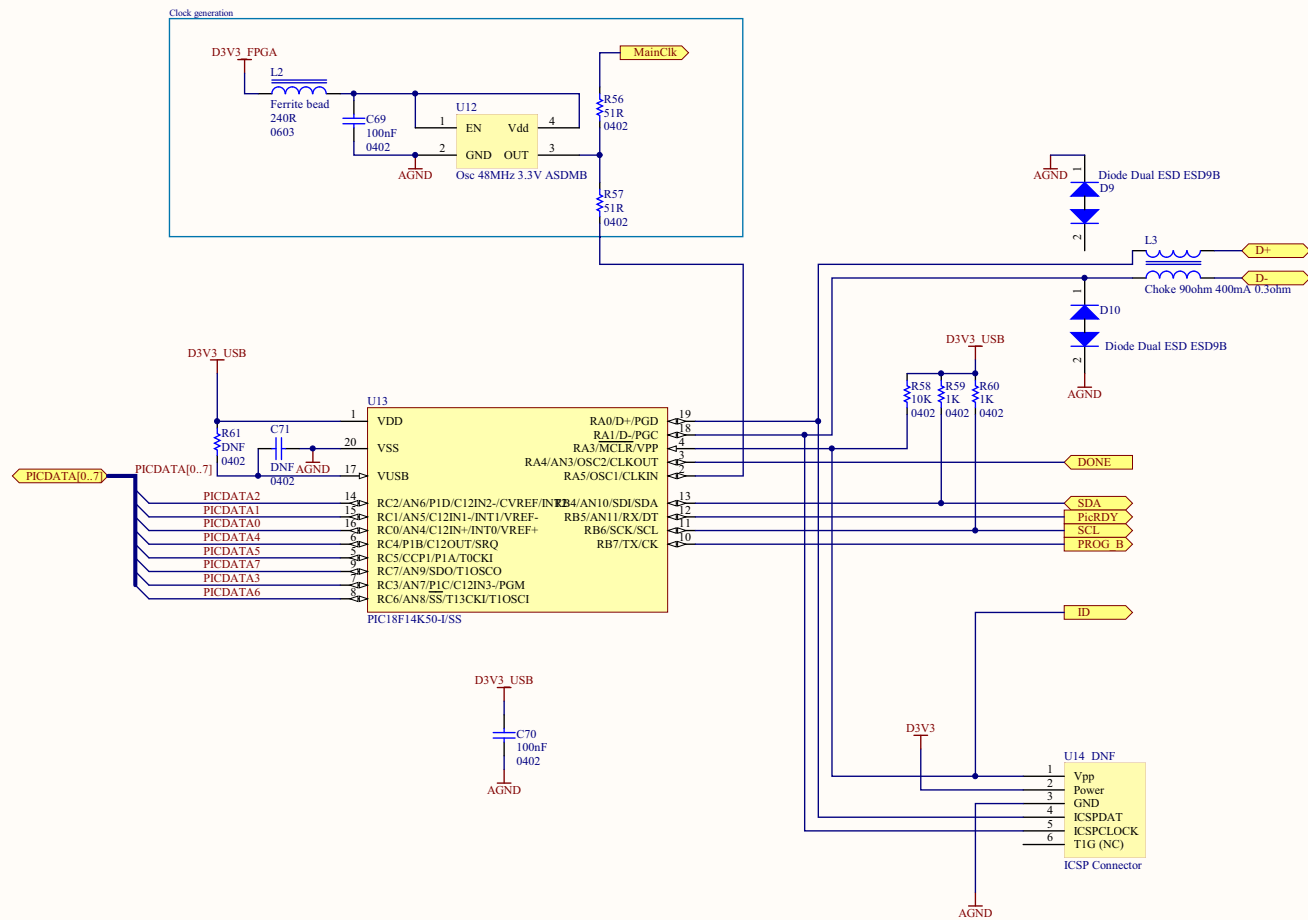
Schematics

Dec rev 1.0

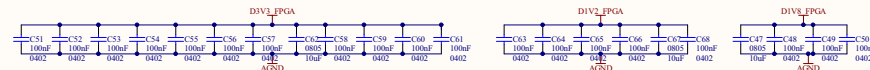
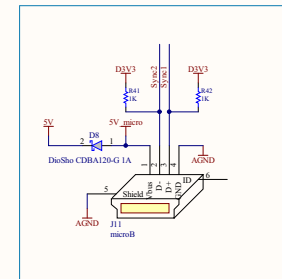
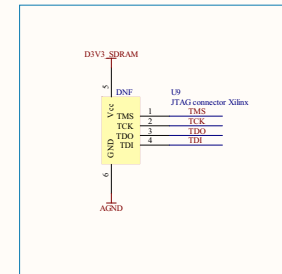
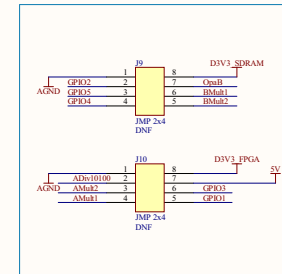
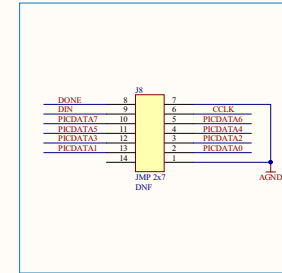
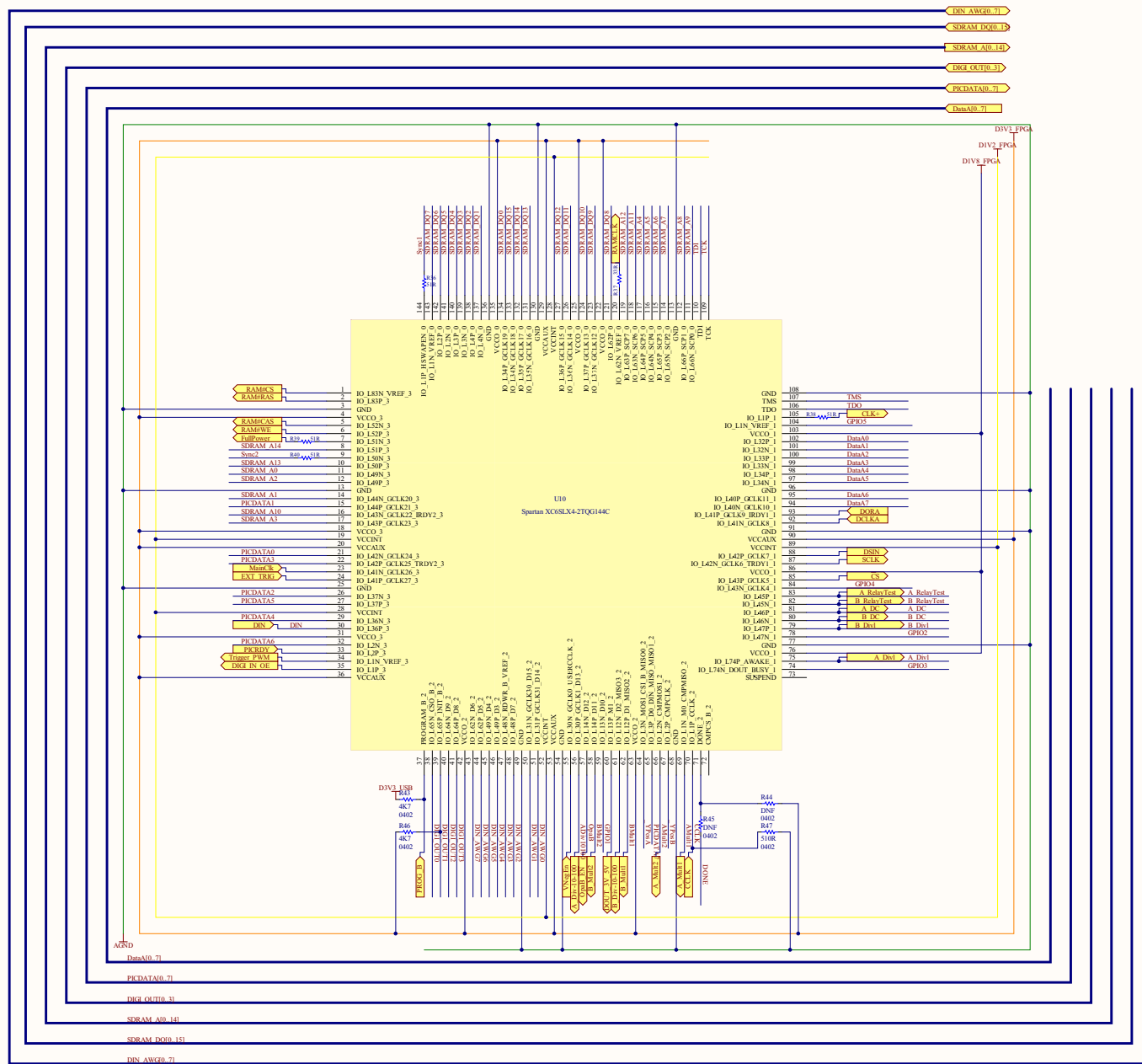
Contents

P2	TopLevel
P3	USB Controller
P4	FPGA
P5	SDRAM
P6	AUX Connector
P7	DAC
P8	ADC

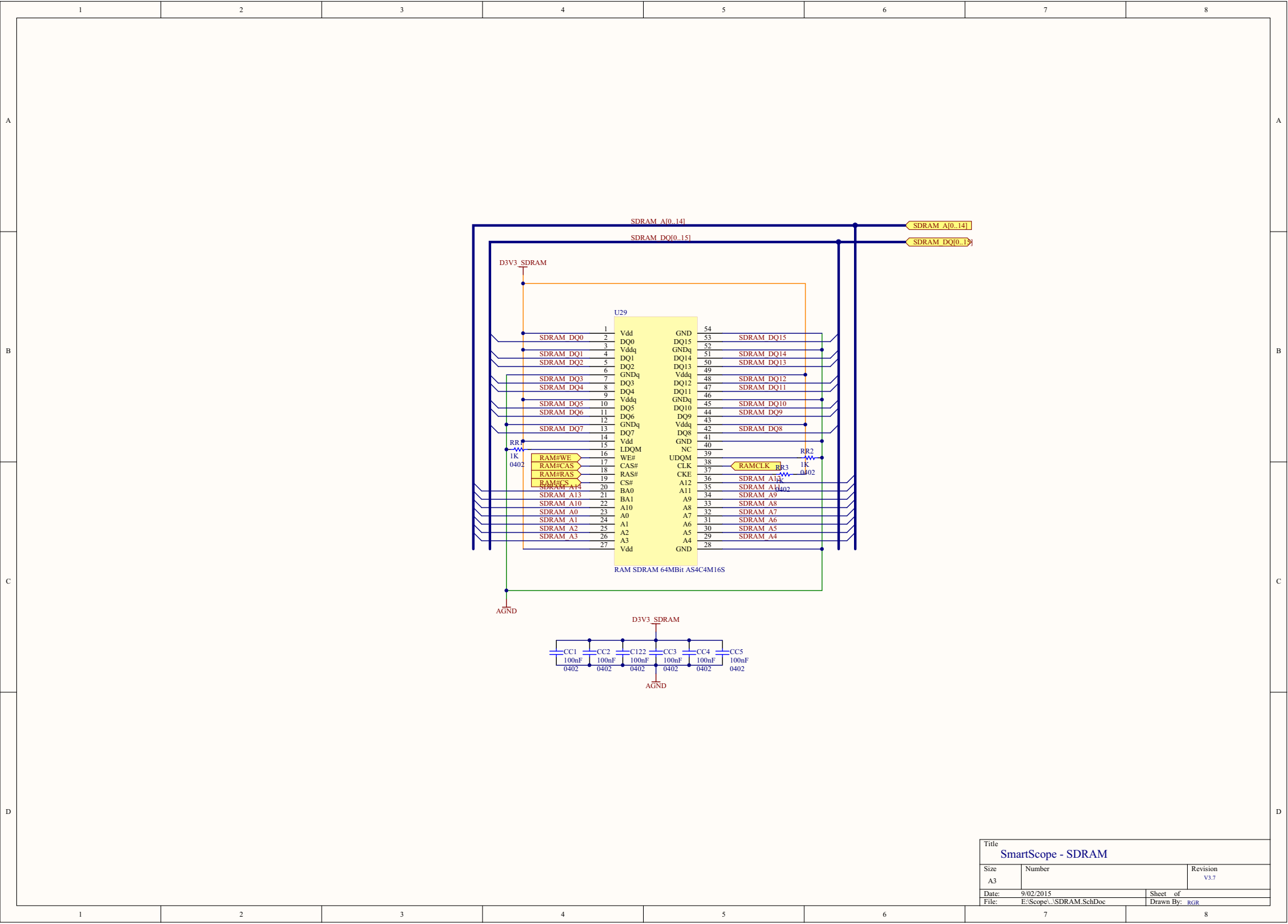


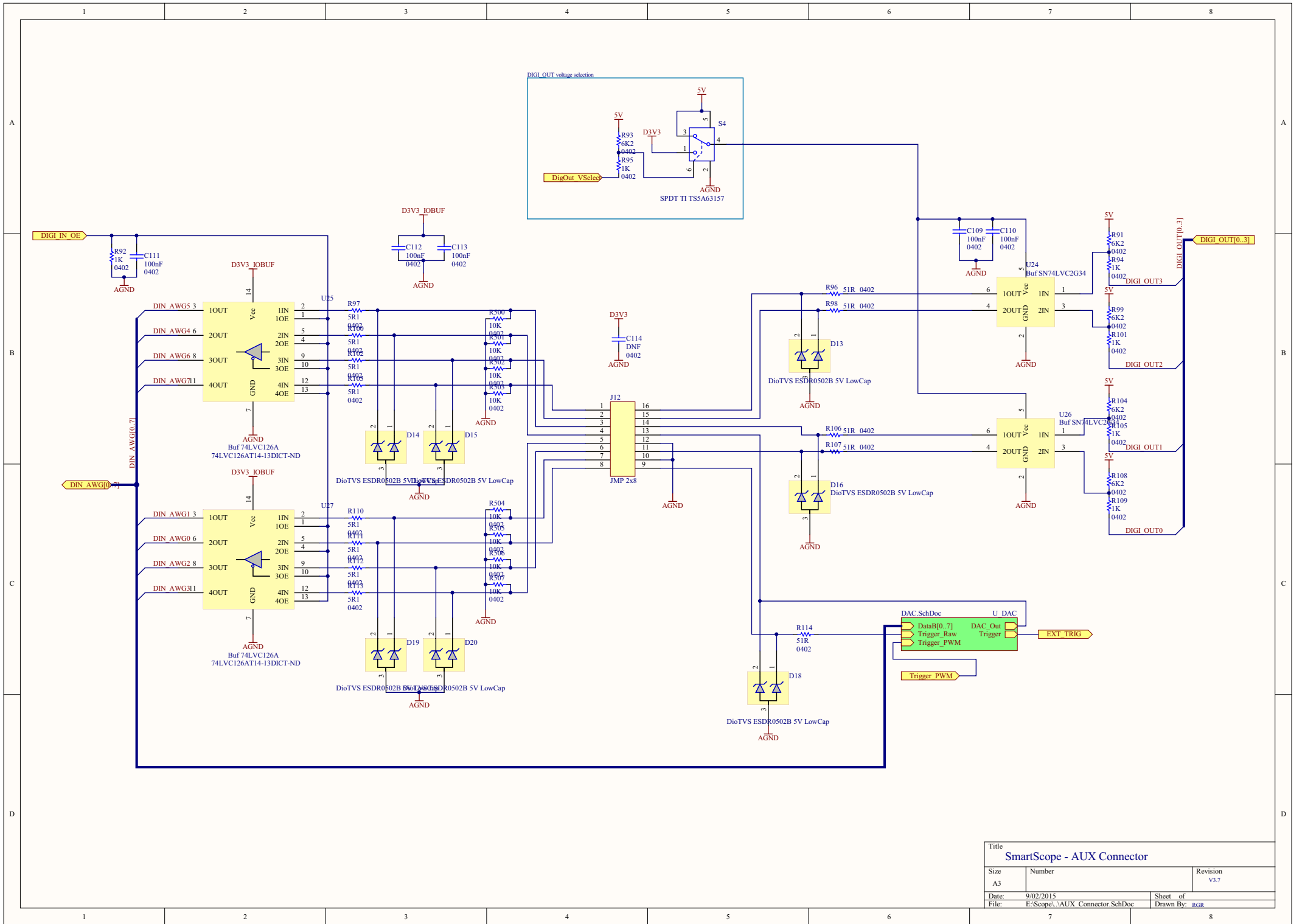


Title		
SmartScope - USB Microcontroller		
Size	Number	Revision
A3		V3.7
Date:	9/02/2015	Sheet of
File:	E:\Scope\...\PIC USB.SchDoc	Drawn By: RGR

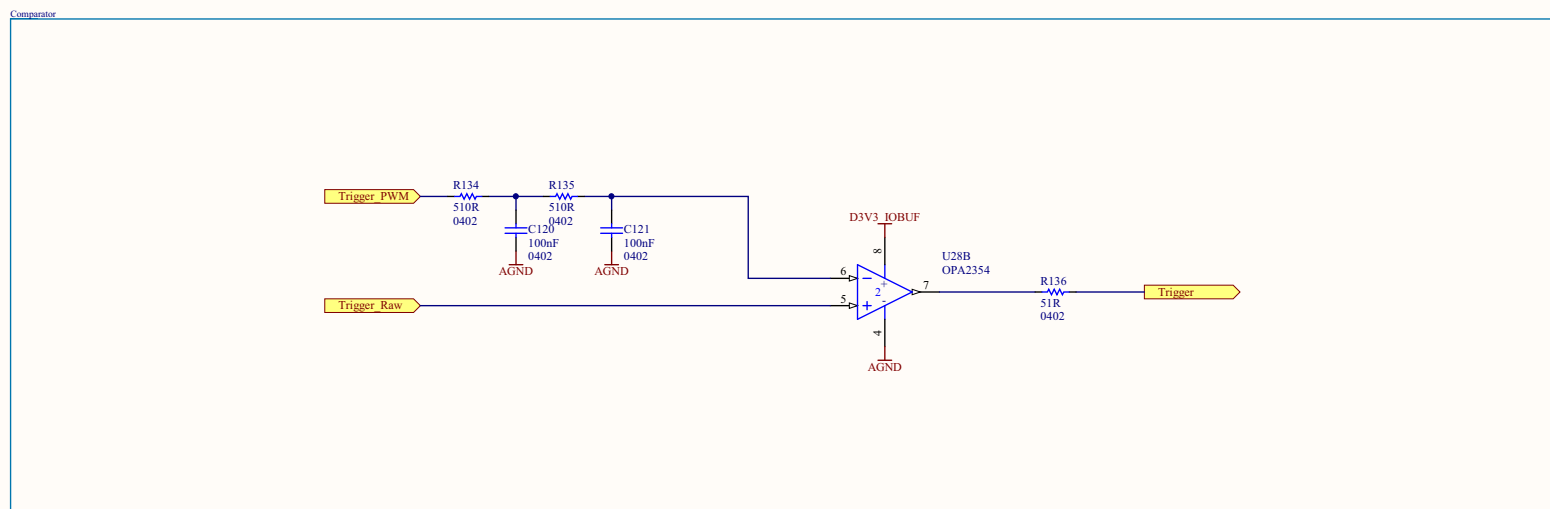
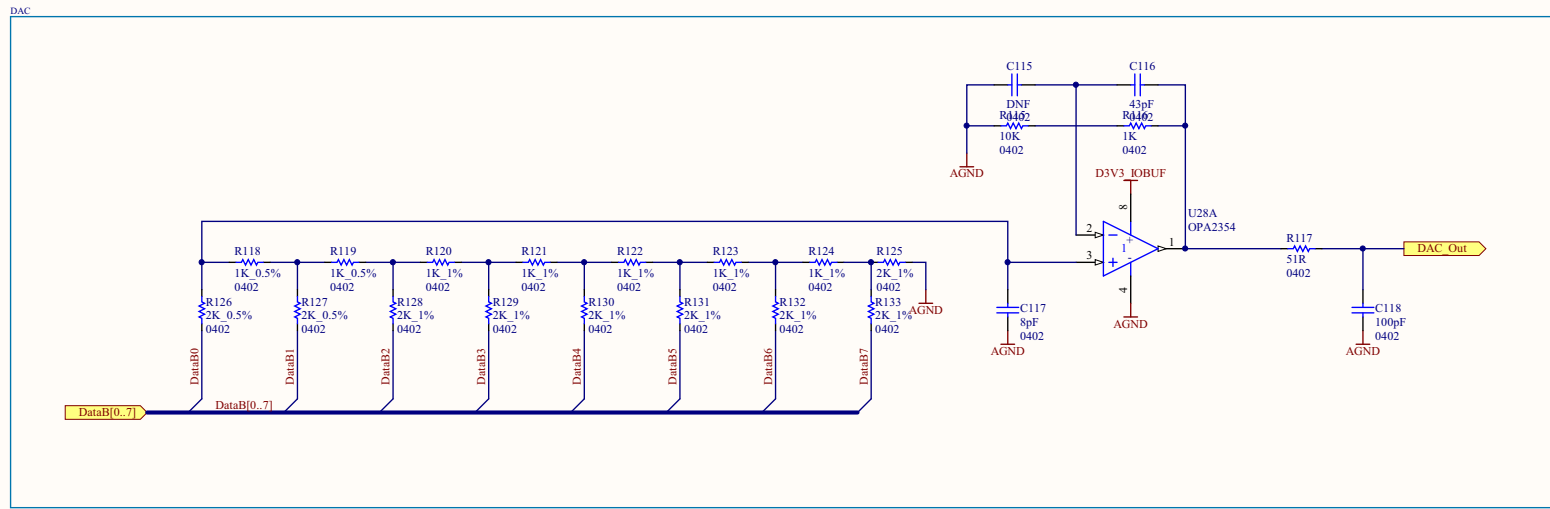


Title		
SmartScope - FPGA		
Size	Number	Revision
A2		V3.1
Date:	9/12/2015	Sheet of
File:	E:\Scope_FPGA_IDs_SchDoc	Drawn By: nrm

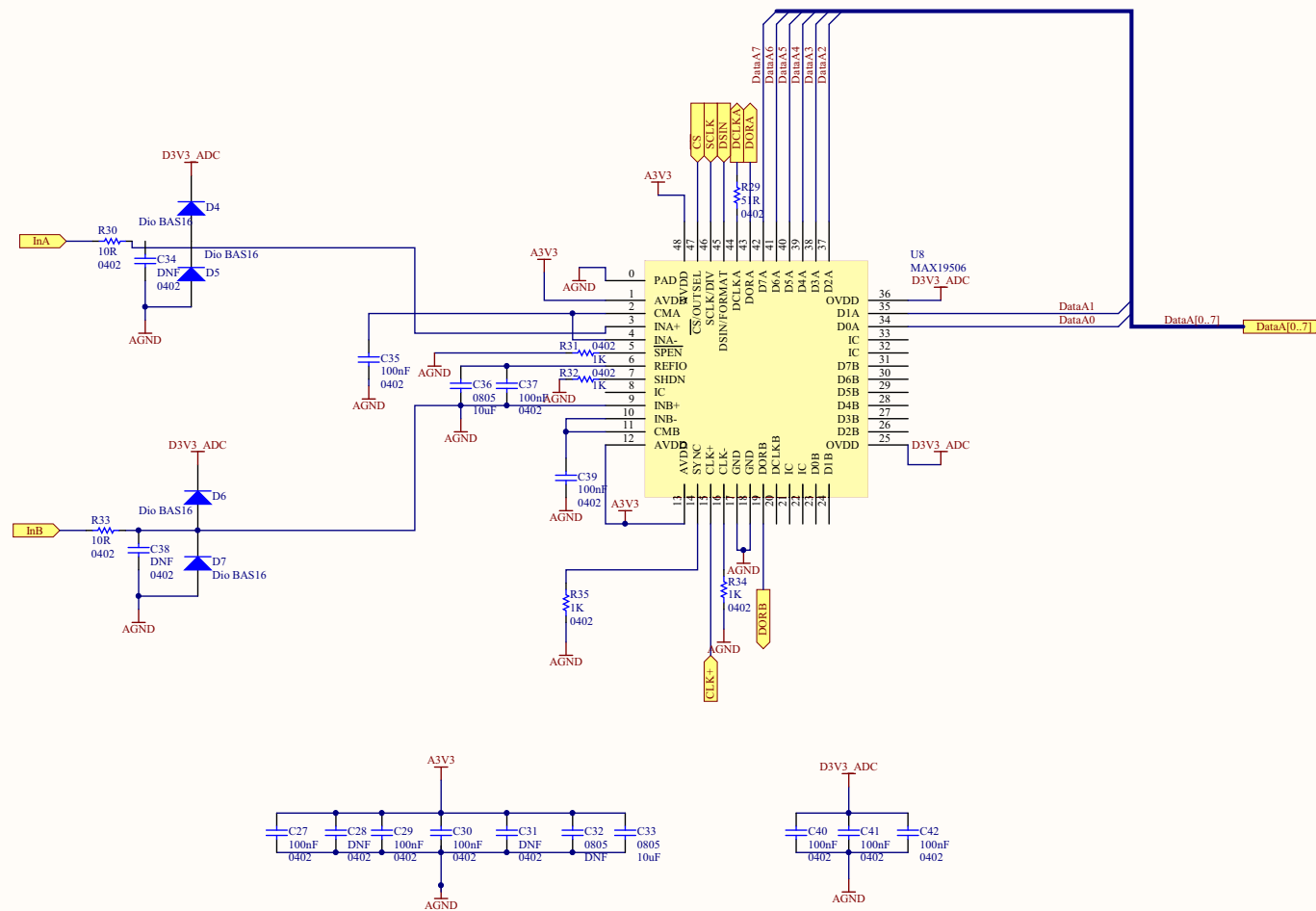




Title		
SmartScope - AUX Connector		
Size	Number	Revision
A3		V3.7
Date:	9/02/2015	Sheet of
File:	E:\Scope\...\AUX_Connector.SchDoc	Drawn By: RGR



Title		
SmartScope - DAC		
Size	Number	Revision
A3		V3.7
Date:	9/02/2015	Sheet of
File:	E:\Scope\...\DAC.SchDoc	Drawn By: RGR



Title		
SmartScope - ADC		
Size	Number	Revision
A3		V3.7
Date:	9/02/2015	Sheet of
File:	E:\Scope\...\ADC.SchDoc	Drawn By: RGR